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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Date Submitted: June 10, 2005 (Use as many sheets as necessary)				Complete if Known	
Application Number				10/755,763	
Filing Date				01/12/2004	
First Named Inventor				Qi Xiang	
Group Art Unit				2812	
Examiner Name				To be determined	
Attorney Docket Number				039153-0684 (H1719)	
Sheet	1	of	1		

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
JMM	A1	2004/0169239	A1	Rim	09-02-2004	
	A2	2003/0227036	A1	Sugiyama et al.	12-11-2003	
	A3	6,645,797	B1	Buynoski et al.	11-11-2003	
	A4	2003/0201458	A1	Clark et al.	10-30-2003	
	A5	6,197,641	B1	Hergenrother et al.	03-06-2001	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
JMM	A6	WO	2003/105189	A2	Lochtefeld et al.	12-18-2003		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
JMM	A7	International Search Report; International Application No. PCT/US2004/043106; mailed April 12, 2005; 4 pages.	
	A8	Written Opinion of the International Searching Authority for International Application No. PCT/US2004/043106; 8 pages.	
	A9	YANG, FU-LIANG, ET AL.; Strained FIP-SOI (FinFET/FD/PD-SOI) for Sub-65 nm CMOS Scaling; 2003 Symposium on VLSI Technology Digest of Technical Papers; page 137.	
JMM	A10	YIN, HAIZHOU, ET AL.; Fully-depleted Strained-Si on Insulator NMOSFETs without Relaxed SiGe Buffers; ©2003 IEEE; pages. 53-56.	

001.1836651.1

Examiner Signature	<i>Ex</i> <i>AA</i>	Date Considered	11/26/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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